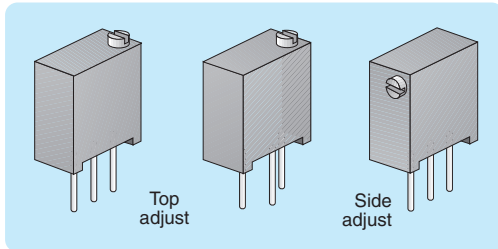


MURATA type PV36 ($\frac{3}{8}$ " square)

A range of $\frac{3}{8}$ inch square, multiturn, cermet trimmers housed in a sealed moulded body suitable for board washing. The trimmers feature 25 turn adjustment for precision setting and a clutch mechanism to prevent excessive wiper rotation. Choice of 3 styles with industry standard packages and footprint.

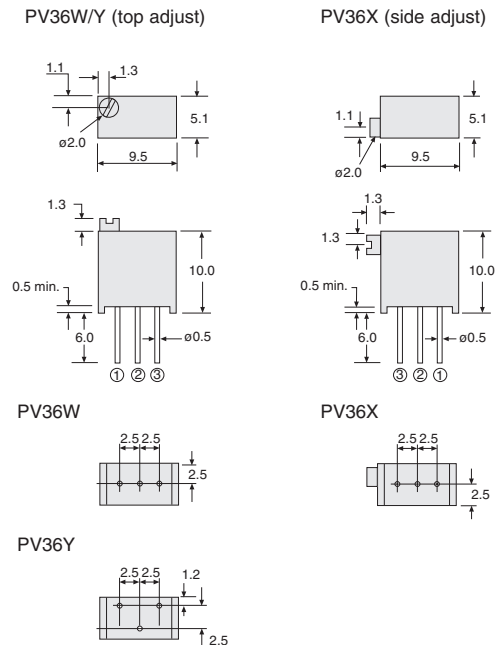


- ◆ Industry standard $\frac{3}{8}$ " square body
- ◆ Cermet element
- ◆ Sealed for board washing
- ◆ Multiturn (25 turns), top or side adjust
- ◆ Choice of 3 styles in common footprints
- ◆ Clutch mechanism prevents excessive wiper rotation

Specification

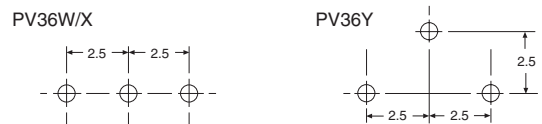
Power rating 0.5 Watt at 70°C
 derate to zero at 125°C
 Resistance tolerance $\pm 10\%$
 Temperature coefficient $\pm 100\text{ppm}/^\circ\text{C}$ max.
 Operating temperature range -55°C to $+125^\circ\text{C}$
 Max. working voltage 300V
 End resistance 2Ω max.
 Electrical travel 25 turns
 Rotational life 200 cycles

Dimensions (mm)



Value (Ω)	Manf. Part No. & Anglia Order Code		
	PV36W Top adjust	PV36X Side adjust	PV36Y Top adjust
10R	PV36W100C01B00	PV36X100C01B00	PV36Y100C01B00
20R	PV36W200C01B00	PV36X200C01B00	PV36Y200C01B00
50R	PV36W500C01B00	PV36X500C01B00	PV36Y500C01B00
100R	PV36W101C01B00	PV36X101C01B00	PV36Y101C01B00
200R	PV36W201C01B00	PV36X201C01B00	PV36Y201C01B00
500R	PV36W501C01B00	PV36X501C01B00	PV36Y501C01B00
1K	PV36W102C01B00	PV36X102C01B00	PV36Y102C01B00
2K	PV36W202C01B00	PV36X202C01B00	PV36Y202C01B00
5K	PV36W502C01B00	PV36X502C01B00	PV36Y502C01B00
10K	PV36W103C01B00	PV36X103C01B00	PV36Y103C01B00
20K	PV36W203C01B00	PV36X203C01B00	PV36Y203C01B00
25K	PV36W253C01B00	PV36X253C01B00	PV36Y253C01B00
50K	PV36W503C01B00	PV36X503C01B00	PV36Y503C01B00
100K	PV36W104C01B00	PV36X104C01B00	PV36Y104C01B00
200K	PV36W204C01B00	PV36X204C01B00	PV36Y204C01B00
250K	PV36W254C01B00	PV36X254C01B00	PV36Y254C01B00
500K	PV36W504C01B00	PV36X504C01B00	PV36Y504C01B00
1M	PV36W105C01B00	PV36X105C01B00	PV36Y105C01B00
2M	PV36W205C01B00	PV36X205C01B00	PV36Y205C01B00

Footprint



Circuit Diagram

