

Low power quad voltage comparator

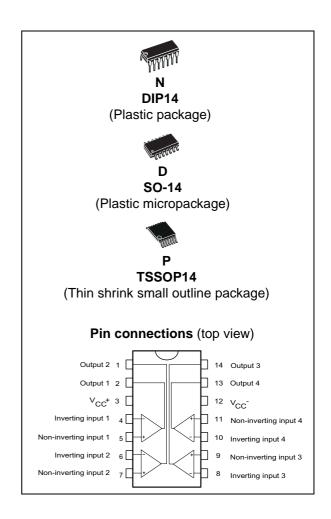
Features

- Wide single supply voltage range or dual supplies for all devices: +2V to +36V or ±1V to ±18V
- Very low supply current (1.1mA) independent of supply voltage (1.4mW/comparator at +5V)
- Low input bias current: 25nA typ.
- Low input offset current: ±5nA typ.
- Input common-mode voltage range includes negative rail
- Low output saturation voltage: 250mV typ. (I_O = 4mA)
- Differential input voltage range equal to the supply voltage
- TTL, DTL, ECL, MOS, CMOS compatible outputs

Description

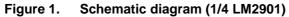
This device consists of four independent precision voltage comparators. All these comparators are designed specifically to operate from a single supply over a wide range of voltages. Operation from split power supplies is also possible.

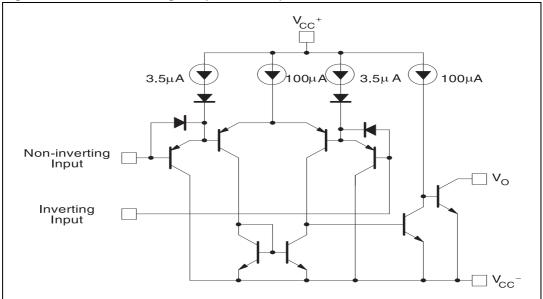
These comparators also have a unique characteristic in that the input common-mode voltage range includes the negative rail even though operated from a single power supply voltage.



Schematic diagram LM2901

1 Schematic diagram





2 Absolute maximum ratings and operating conditions

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit V	
V _{CC}	Supply voltage	±18 to 36		
V _{id}	Differential input voltage	±36	V	
V _{in}	Input voltage	-0.3 to +36	V	
	Output short-circuit to ground (1)			
R _{thja}	Thermal resistance junction to ambient ⁽²⁾ DIP14 SO-14 TSSOP14	80 105 100	°C/W	
R _{thjc}	Thermal resistance junction to case ⁽²⁾ DIP14 SO-14 TSSOP14	33 31 32		
Tj	Maximum junction temperature	+150	°C	
T _{stg}	Storage temperature range	-65 to +150	°C	
	HBM: human body model ⁽³⁾	500	V	
ESD	MM: machine model ⁽⁴⁾	100	V	
	CDM: charged device model ⁽⁵⁾	1500	V	

- Short-circuits from the output to V_{CC}⁺ can cause excessive heating and eventual destruction. The maximum output current is approximately 20mA, independent of the magnitude of V_{CC}⁺.
- Short-circuits can cause excessive heating. Destructive dissipation can result from simultaneous short-circuits on all amplifiers. All values are typical.
- 3. Human body model: A 100pF capacitor is charged to the specified voltage, then discharged through a 1.5kΩ resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.
- 4. Machine model: A 200pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5Ω). This is done for all couples of connected pin combinations while the other pins are floating.
- Charged device model: all pins and the package are charged together to the specified voltage and then discharged directly to the ground through only one pin. This is done for all pins.

Table 2. Operating conditions

Symbol	Parameter	Value	Unit
V _{CC}	Supply voltage	2 to 32 ±1 to ±16	V
V _{icm}	Common mode input voltage range $T_{min} \le T_{amb} \le T_{max}$	0 to (V _{CC} ⁺ -1.5) 0 to (V _{CC} ⁺ -2)	V
T _{oper}	Operating free-air temperature range	-40 to +125	°C

Electrical characteristics LM2901

3 Electrical characteristics

Table 3. Electrical characteristics at $V_{CC}^+ = 5V$, $V_{CC}^- = GND$, $T_{amb} = 25^{\circ}C$ (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit
V _{io}	Input offset voltage $^{(1)}$ $T_{min} \le T_{amb} \le T_{max}$		1	7 15	mV
I _{io}	Input offset current $T_{min} \le T_{amb} \le T_{max}$		5	50 150	nA
I _{ib}	Input bias current $(I_1^+ \text{ or } I_1^-)^{(2)}$ $T_{\text{min}} \le T_{\text{amb}} \le T_{\text{max}}$		25	250 400	nA
A _{vd}	Large signal voltage gain $(V_{CC} = 15V, R_L = 15k\Omega, V_o = 1 \text{ to } 11V)$	25	200		V/mV
Icc	Supply current (all comparators) $V_{CC} = +5V, \text{ no load}$ $V_{CC} = +30V, \text{ no load}$		1.1 1.3	2 2.5	mA
V _{id}	Differential input voltage ⁽³⁾			V _{CC} ⁺	V
V _{OL}	Low level output voltage $V_{id} = -1V, I_{sink} = 4mA$ $T_{min} \le T_{amb} \le T_{max}$		250	400 700	mV
Іон	$\begin{aligned} & \text{High level output current} \\ & (V_{CC} = V_o = 30 \text{V}, V_{id} = 1 \text{V}) \\ & T_{min} \leq T_{amb} \leq T_{max} \end{aligned}$		0.1	1	nΑ μΑ
I _{sink}	Output sink current ($V_{id} = -1V, V_0 = 1.5V$)	6	16		mA
t _{res}	Small signal response time ⁽⁴⁾ $(R_L = 5.1 \text{k}\Omega \text{ connected to V}_{CC}^+)$		1.3		μs
t _{rel}	Large signal response time ⁽⁵⁾ TTL input (V _{ref} =+1.4 V, R _L =5.1kΩ to V _{CC} ⁺) Output signal at 50% of final value Output signal at 95% of final value			500 1	ns µs

^{1.} At output switch point, $V_O \approx 1.4V$, $R_S = 0$ with V_{CC}^+ from 5V to 30V, and over the full input common-mode range (0V to V_{CC}^+ –1.5V).

5. Maximum values are guaranteed by design.

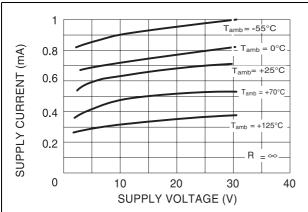
^{2.} The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output, so there is no loading charge on the reference of input lines.

^{3.} The response time specified is for a 100mV input step with 5mV overdrive.

^{4.} Positive excursions of input voltage may exceed the power supply level. As long as the other voltage remains within the common-mode range, the comparator will provide a proper output state. The low input voltage state must not be less than –0.3V (or 0.3V below the negative power supply, if used).

Figure 2. Supply current vs. supply voltage

Figure 3. Input current vs. supply voltage



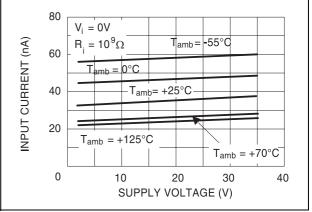
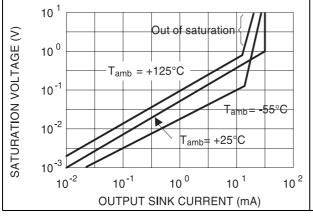


Figure 4. Output saturation voltage vs. output current

Figure 5. Response time for various input overdrives - negative transition



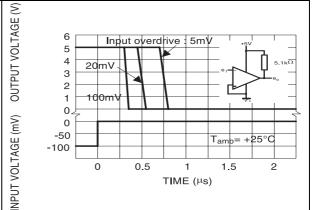
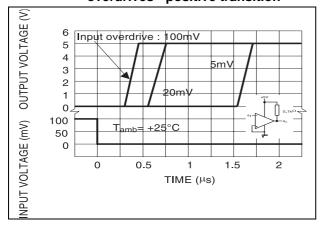


Figure 6. Response time for various input overdrives - positive transition



4 Typical applications schematics

Figure 7. Basic comparator

 $V_{CC}^{+} = 5V$ $+V_{(ref)} \bigcirc \qquad \qquad 1/4$ $-V_{(ref)} \bigcirc \qquad \qquad V_{O}$

Figure 8. Driving CMOS

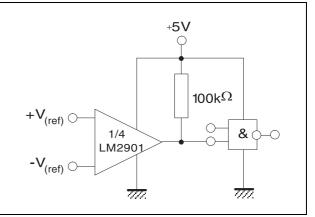


Figure 9. Driving TTL

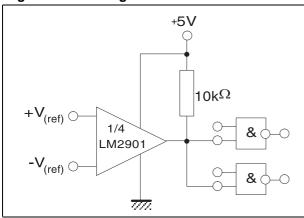


Figure 10. Low frequency op-amp

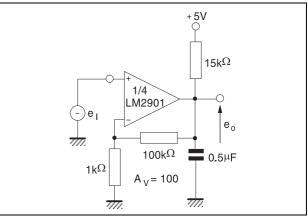


Figure 11. Low frequency op-amp

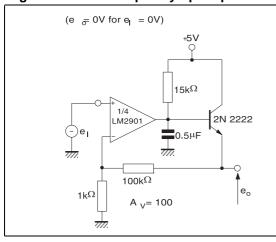


Figure 12. Transducer amplifier

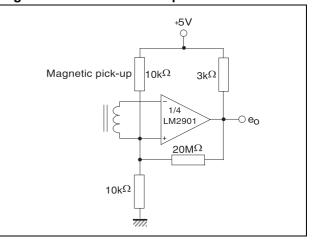


Figure 13. Low frequency op- amp with offset adjust

Figure 14. Zero crossing detector (single power supply)

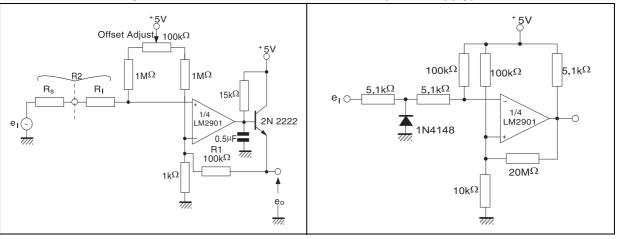


Figure 15. Limit comparator

Figure 16. Split-supply applications - zero crossing detector

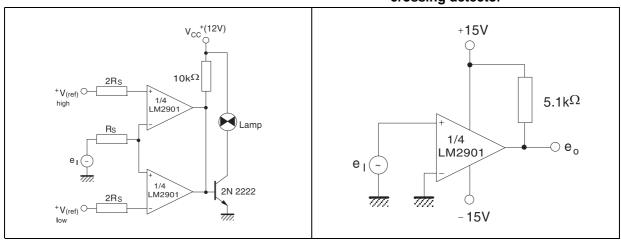
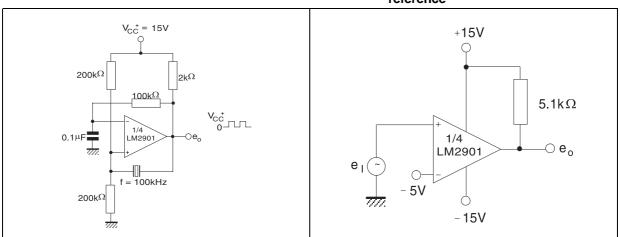


Figure 17. Crystal controlled oscillator

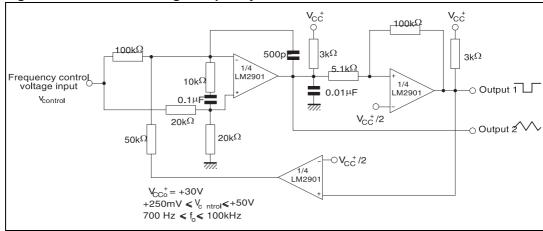
Figure 18. Comparator with a negative reference



O V_{CC} = +15V зкΩ 200kΩ <u>10kΩ</u> 1/4 LM2901 10MΩ _10kΩ 10k Ω 1/4 LM2901 0.001µ INPUT GATING SIGNAL зкΩ V_{CC} + 10MΩ 51k Ω $10 k \Omega$ to t1 51k Ω t₀ t₁ t₂ t₃

Figure 19. Time delay generator





8/13

LM2901 **Package information**

5 **Package information**

In order to meet environmental requirements, STMicroelectronics offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an STMicroelectronics trademark. ECOPACK specifications are available at: www.st.com.

5.1 **DIP14** package information

Figure 21. DIP14 package mechanical drawing

Table 4. DIP14 package mechanical data

	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
a1	0.51			0.020		
В	1.39		1.65	0.055		0.065
b		0.5			0.020	
b1		0.25			0.010	
D			20			0.787
Е		8.5			0.335	
е		2.54			0.100	
e3		15.24			0.600	
F			7.1			0.280
I			5.1			0.201
L		3.3			0.130	
Z	1.27		2.54	0.050		0.100

Package information LM2901

5.2 SO-14 package information

Figure 22. SO-14 package mechanical drawing

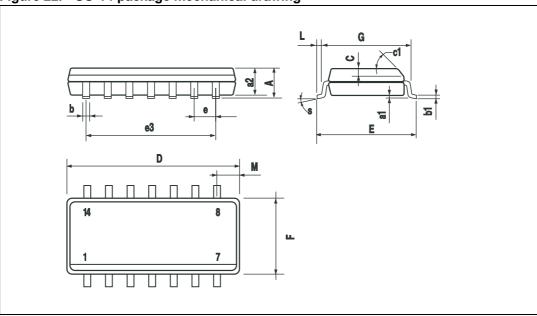


Table 5. SO-14 package mechanical data

	Dimensions						
Ref.		Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.	
А			1.75			0.068	
a1	0.1		0.2	0.003		0.007	
a2			1.65			0.064	
b	0.35		0.46	0.013		0.018	
b1	0.19		0.25	0.007		0.010	
С		0.5			0.019		
c1			45°	(typ.)			
D	8.55		8.75	0.336		0.344	
E	5.8		6.2	0.228		0.244	
е		1.27			0.050		
e3		7.62			0.300		
F	3.8		4.0	0.149		0.157	
G	4.6		5.3	0.181		0.208	
L	0.5		1.27	0.019		0.050	
М			0.68			0.026	
S		•	8° (ı	max.)	•		

LM2901 Package information

5.3 TSSOP14 package information

Figure 23. TSSOP14 package mechanical drawing

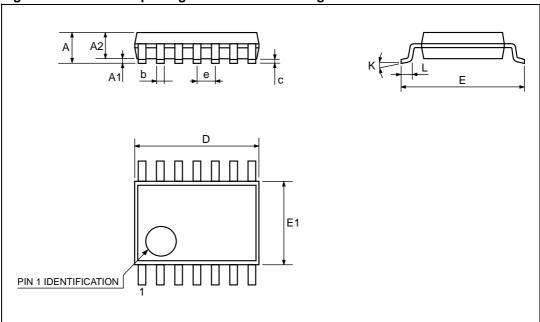


Table 6. TSSOP14 package mechanical data

	Dimensions						
Ref.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α			1.2			0.047	
A1	0.05		0.15	0.002	0.004	0.006	
A2	0.8	1	1.05	0.031	0.039	0.041	
b	0.19		0.30	0.007		0.012	
С	0.09		0.20	0.004		0.0089	
D	4.9	5	5.1	0.193	0.197	0.201	
E	6.2	6.4	6.6	0.244	0.252	0.260	
E1	4.3	4.4	4.48	0.169	0.173	0.176	
е		0.65 BSC			0.0256 BSC		
K	0°		8°	0°		8°	
L1	0.45	0.60	0.75	0.018	0.024	0.030	

11/13

Ordering information LM2901

6 Ordering information

Table 7. Order codes

Order code	Temperature range	Package	Packing	Marking
LM2901N		DIP14	Tube	LM2901N
LM2901D LM2901DT		SO-14	Tube or tape & reel	2901
LM2901PT	-40°C to +125°C	TSSOP14	Tape & reel	
LM2901YD ⁽¹⁾ LM2901YDT ⁽¹⁾		SO-14 (Automotive grade)	Tube or tape & reel	2901Y
LM2901YPT ⁽²⁾		TSSOP14 (Automotive grade)	Tape & reel	25011

Qualified and characterized according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 & Q 002 or equivalent.

7 Revision history

Table 8. Document revision history

Date	Revision	Changes
Jan-2002	1	Initial release.
Jul-2005	2	1 - PPAP references inserted in the datasheet see <i>Table : Order codes on page 1</i> . 2 - ESD protection inserted in <i>Table 1 on page 3</i> .
Oct-2005	3	The following changes were made in this revision: - PPAP part number added in table <i>Order codes on page 1</i> . - Formatting changes throughout.
18-Jul-2006	4	ESD HBM value corrected in Table 1 on page 3.
19-Dec-2007	5	Added R _{thja} and R _{thjc} parameters to <i>Table 1: Absolute maximum ratings</i> . Added footnotes for ESD parameters. Removed V _{icm} parameter from electrical characteristics in <i>Table 3</i> . Reformatted package information in <i>Section 5</i> . Added footnotes for automotive grade parts in <i>Table 7: Order codes</i> .

^{2.} Qualification and characterization according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 & Q 002 or equivalent are on-going.

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